## BPMs for LHC sector test

- Hardware acquires individual LHC bunches (@ 40 MHz)
- LHC system is exact copy of BPMI (TI8) trajectory system in terms of hardware
- We can provide calibrated position data:
  - Bunch-by-Bunch (B selected bunches over T selected turns)
  - Turn-by-turn (average of all valid bunches in 1 turn)
  - Closed-orbit (average of all valid bunches over N turns)
- Hardware works in one of two acquisition modes:
  - Synchronous (needs BST timing and correct phases)
  - Asynchronous (safer, but 40MHz information lost)
- Sensitivity switch in the tunnel controlled over WorldFIP to handle big intensity variations (high for pilots, low for I > ??)
- BPM sum signal or not and how to handle ??

## BPM timing events:

- We will need these LHC timing events:
  - Fore-warning before FIRST LHC injection takes place
    - Setup hardware modules (phases) etc
  - Short warning to arm acquisition modules before each LHC injection LHC. We can publish the data for each injection based on this event

## **BPM properties (from TI8)**

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• We would add to this property:

- cycleStamp (UTC timestamp linked to ? for LHC)